

L Number	Hits	Search Text	DB	Time stamp
1	0	((("core voltage" and clock) and (cpu or processor or microprocessor)) and (low or sleep)) and (reduc\$3 or cut\$4)	JPO	2003/07/27 10:21
2	0	((("core voltage" and clock) and (cpu or processor or microprocessor)) and (low or sleep)) and (reduc\$3 or cut\$4)	JPO	2003/07/27 10:16
6	0	((("core voltage" and clock) and (cpu or processor or microprocessor)) and (low or sleep))	IBM_TDB	2003/07/27 10:16
3	1	((("core voltage" and clock) and (cpu or processor or microprocessor)) and (low or sleep))	EPO; JPO	2003/07/27 10:22
7	2	((("core voltage" and clock) and (cpu or processor or microprocessor)) and (low or sleep))	EPO; JPO	2003/07/27 10:19
10	2	"6202104"	USPAT	2003/07/27 10:19
11	0	((("core voltage" and clock) and (cpu or processor or microprocessor)) and (clock near3 disable))	JPO	2003/07/27 10:21
12	2	((("core voltage" and clock) and (cpu or processor or microprocessor)) and (clock near3 disable))	USPAT	2003/07/27 10:21
13	3381	("central processing unit" or cpu or processor or microprocessor) near4 (stop\$3 or disable\$1)	EPO; JPO	2003/07/27 10:24
16	12606	("central processing unit" or cpu or processor or microprocessor) near4 (stop\$3 or disable\$1)	USPAT	2003/07/27 10:24
17	0	((("central processing unit" or cpu or processor or microprocessor) near4 (stop\$3 or disable\$1)) and ("core voltage"))	EPO; JPO	2003/07/27 10:24
20	13	((("central processing unit" or cpu or processor or microprocessor) near4 (stop\$3 or disable\$1)) and ("core voltage"))	USPAT	2003/07/27 10:24
21	315	(stop\$3 or disable\$1) near4 clock with (cpu or processor or microprocessor or "central processing unit")	JPO	2003/07/27 10:46
22	28	((stop\$3 or disable\$1) near4 clock with (cpu or processor or microprocessor or "central processing unit")) with (reduc\$3 or lower\$3)	JPO	2003/07/27 10:39
24	120	(stop\$3 or disable\$1) near4 clock with (cpu or processor or microprocessor or "central processing unit") with (reduc\$3 or lower\$3)	USPAT	2003/07/27 10:48
23	20	(stop\$3 or disable\$1) near4 clock with (cpu or processor or microprocessor or "central processing unit")	EPO	2003/07/27 10:47
25	2	((stop\$3 or disable\$1) near4 clock with (cpu or processor or microprocessor or "central processing unit") with (reduc\$3 or lower\$3)) and (voltage adj1 regulator)	USPAT	2003/07/27 11:10
26	3	"5844435"	USPAT	2003/07/27 10:54
27	9	monitoring near6 (stop adj1 clock)	USPAT	2003/07/27 11:10

	Type	BRS	Search Text
1	BRS	321	"core voltage"
2	BRS	30	"core voltage"
3	BRS	1	"core voltage" and clock
4	BRS	73	"core voltage" and clock
5	BRS	47	("core voltage" and clock) and (cpu or processor or microprocessor)
6	BRS	44	((("core voltage" and clock) and (cpu or processor or microprocessor)) and (low or sleep)
7	BRS	41	((("core voltage" and clock) and (cpu or processor or microprocessor)) and (low or sleep)) and (reduc\$3 or cut\$4)
8	BRS	16	"core voltage" with regulator with (cpu or processor or microprocessor)